RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SY*-A017A/E	Rev.	1.00
Title	Additional information for security MPU use in MPU chapter, S5D9 User's Manual Rev.1.00		Information Category	Technical Notification		
Applicable Product	Renesas Synergy™ S5 Series S5D9	Lot No.	Reference Document	S5D9 Microcontroller User's Manual, Rev.1.00		
		All lots				

We would inform you of corrections about S5D9 Microcontroller user's manual below.

<Details of corrections>

When using Security MPU, additional describes a note on setting of Security MPU Program Counter Start Address Register.

1. 16.6.1.3 Security MPU Region 0 Start Address Register (SECMPUS0)

   [Before]

   The SECMPUS0 register specifies the start address where the region starts.

   [After]

   The SECMPUS0 register specifies the start address where the region starts.

   Setting of the vector table area is prohibited.

2. 16.6.1.5 Security MPU Region 1 Start Address Register (SECMPUS1)

   [Before]

   The SECMPUS1 register specifies the start address where the region starts.

   [After]

   The SECMPUS1 register specifies the start address where the region starts.

   Setting of the stack area and the vector table are prohibited.

3. 16.6.1.1 Security MPU Program Counter Start Address Register (SECMPUPCSn) (n = 0, 1)

   [Before]

   The SECMPUPCSn register specifies the start address where the region starts.

   Setting of the memory mirror space (0200 0000h to 027F FFFFh) for MMF is prohibited.

   [After]

   The SECMPUPCSn register specifies the start address where the region starts.

   Setting of the memory mirror space (0200 0000h to 027F FFFFh) for MMF is prohibited.

   A time interval of greater than 12 bytes is required between the last instruction of the non-secure program and the first instruction of the secure program.