

Low Distortion High Value Multilayer Ceramic Capacitors(CF_LD)

UMK325LD105MN-T



■ Features

- Item Summary
1uF±20%, 50V,
X5R, 1210/3225 (EIA/JIS)
- Lifecycle Stage
Mass Production
- Standard packaging quantity (minimum)
Taping Embossed 2000pcs

■ Products characteristics table

Capacitance	1 uF ± 20 %
Case Size (EIA/JIS)	1210/3225
Rated Voltage	50 V
tan δ (max)	10 %
Temperature Characteristic (EIA)	X5R
Operating Temp. Range (EIA)	-55 to +85 °C
High Temperature Loading (% Rated Voltage)	200 %
Insulation Resistance (min)	100 MΩ · μF
RoHS2 Compliance (10 subst.)	Yes
REACH Compliance (173 subst.)	Yes
Halogen Free	Yes
Soldering	Reflow

■ External Dimensions

Dimension L	3.2 ±0.30 mm
Dimension W	2.5 ±0.20 mm
Dimension T	1.9 ±0.20 mm
Dimension e	0.60 ±0.30 mm

2017.04.30

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
 TAIYO YUDEN reserves the right to make change to the Date at any time without notice.
 Before making final selection, please check product specification.

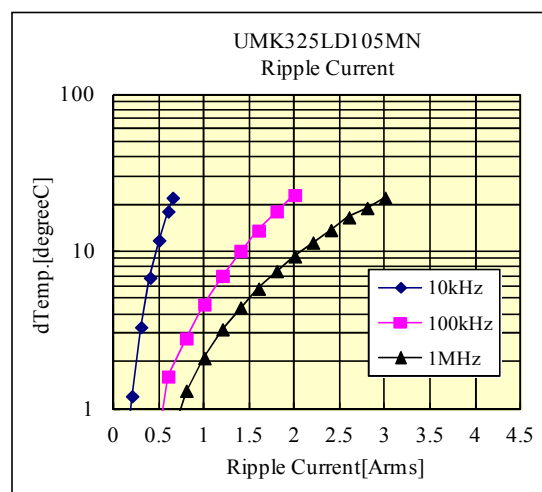
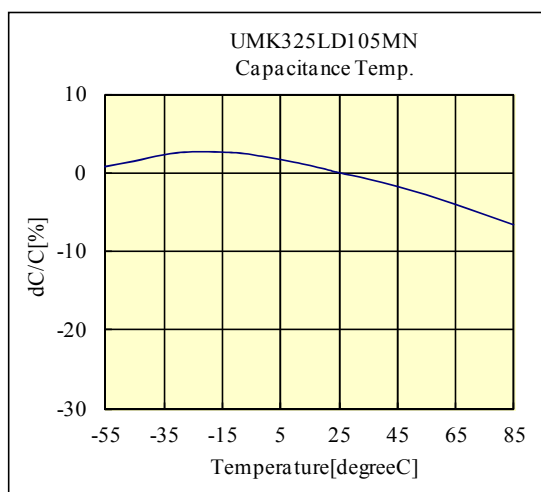
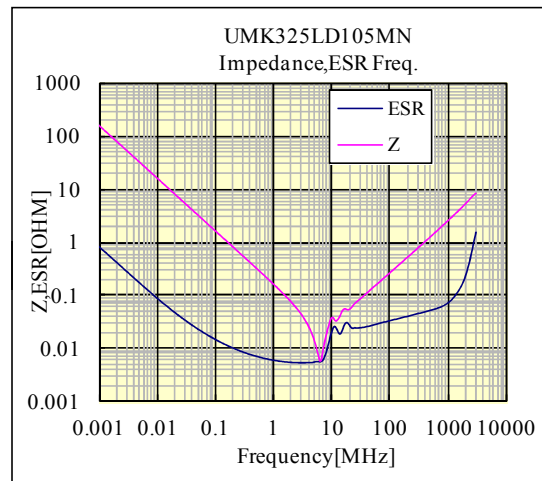
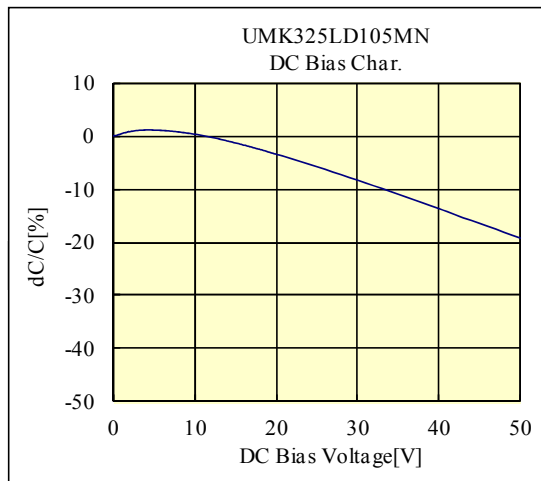
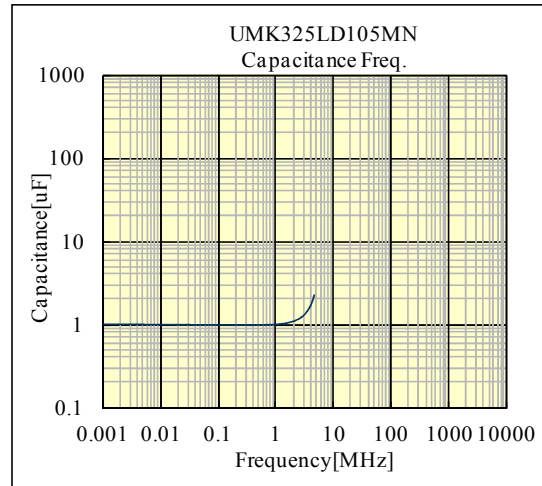
Multi Layer Ceramic Capacitor **TAIYO YUDEN**

-Electrical Characteristics Data- 16CWA02

RoHS compliance product

○UMK325LD105MN

50V 3225 X5R 1 μ F +/- 20% t=2.1mmMAX
50V 1210 X5R 1 μ F +/- 20% t=0.083inchMAX



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